

Abstract

The present invention relates to a controlled current source having a control input, in particular for digital/analogue converters in continuous-time sigma/delta modulators, having a current source (4) with a control input which generates an output current dependent on a control voltage applied to the control input, and having a controller (7) for converting a clock signal into a voltage signal, with the controller (7) being connected to the current source (4) in such a manner that the voltage signal is applied as a control voltage to the control input of the current source (4). The controller is designed to convert the clock signal into a voltage signal which has within a clock duration a reproducible curve ending with a falling flank. Using the present controlled current source in a digital/analogue converter in a feedback branch of a continuous-time sigma/delta modulator permits realizing a sigma/delta modulator which is essentially insensitive to clock jitter.